

AEROSPACE

Space

Electrical  
Engineering



## SATELLITE BACKPLANE DESIGN AND TESTING SUPPORT

### APPROACH

- Integrate with client team and processes
- Design backplane schematic and support layout, including power and high-speed signal interfaces such as DDR and SRIO
- Perform derating analysis, including radiation survivability for EEE components
- Perform detailed HyperLynx simulations for multiple cards in design

### RESULTS

- Created design files for fabrication and documentation
- Wrote signal integrity and timing reports for multiple cards
- Successfully tested hardware alongside client engineers

### KEY TOOLS & TECHNOLOGIES

- Mentor Graphics Xpedition
- HyperLynx

ALTEN TECHNOLOGY